# Using Imake to Configure the X Window System Version 11, Release 4

*Paul DuBois*
*dubois@primate.wisc.edu*

Wisconsin Regional Primate Research Center
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*ABSTRACT*

The X Window System† is a large software project that, despite its size, is remarkable in its portability. Much of this is due to the method employed to configure the X distribution for building and installation: use of a small number of tools and isolation of machine dependencies into a small number of files that can be easily maintained and modified. This document discusses one of those tools, *imake*, and the design of the configuration files used in conjunction with it.

## 1. Introduction

This document describes how *imake* configuration files are set up in the X Window System. It is assumed that you know something about what *imake* is supposed to be used for (in general), and that you're reading this to find out something about how that is accomplished in X (in particular). The description applies to X Version 11, release 4 configuration files, which are organized quite differently than those from previous releases.

Other documentation (from the X11R4 distribution) that you may find useful includes:

- Section 2 of ''X Window System, Version 11 Release 4 Release Notes,'' by Jim Fulton.
- *mit/doc/config/usenixws/paper.ms*: ''Configuration Management in the X Window System,'' also by Jim Fulton.
- *mit/config/README*: ''X Window System Imake Configuration Guide, Release 4.''
- *mit/config/imake.man*: manual page for *imake*.
- *mit/util/makedepend/mkdepend.man*: manual page for *makedepend*.
- *contrib/doc/imake/imake.tex*: ''An *Imake* Tutorial,'' by Mark Moraes. This was written between R3 and R4, but is worthwhile reading nonetheless.

You should also have access to the X configuration files (in *mit/config*). These are not really documentation as such, but it is expected that you have them at hand, for comparison against the descriptions below.

*imake* is generally conceded to have a pretty steep learning curve. The *README* referred to above notes that *imake* ''can be somewhat tricky to master,'' an observation attested to by many who use it. There seem

† X Window System is a trademark of the Massachusetts Institute of Technology.

to be three camps regarding *imake*; those who think it's wonderful, those who think it's wretched, and those who suspect it might be useful (else why would it be used to configure a major publicly-available effort like X?) but are puzzled by it. The present document was written to fill in some of the gaps in the existing documentation, in order to try to swell the ranks of the first group by depleting the membership of the third. (Those who despise *imake*—members of the second group—have good reasons for doing so and the present effort is not likely to sway them. The author of *imake* is in this group.)

Where appropriate, the current X configuration files are compared to those from previous releases to show how limitations of earlier configuration architectures have been eased. There are also occasional comments to indicate how *imake* and the X configuration files might be adapted for use in contexts other than building X.

This document is independent of the efforts of the MIT X Consortium. There are no doubt errors lurking within, both of understanding and of fact; they are my own. Please send corrections, criticisms and comments to the address above.

The overall organization followed here is:

1. Introduction.
2. X configuration tool description—the programs you use.
3. X configuration file description—what each file is used for, how each fits into the configuration process.
4. How to build *imake*.
5. How to build X.
6. Configuration file bugs.
7. How to write your own *Imakefile*. You can read this section without reading the rest of the paper, maybe.
8. How to make mistakes writing an *Imakefile*.

Appendix
: Listing of the symbols used in each configuration file, and what they're used for.

## 2. The tools: imake, makedepend, xmkmf

*imake* is a configuration tool—the main tool used to configure X. It is not a replacement for the *make* program, but it does assume the availability of *make* as the tool used to direct project building and installation. What *imake* provides is an alternative to writing *Makefiles* by hand. The name means ''include-make''—*imake* uses the C preprocessor *cpp*, taking advantage of its include-file and macro-processing facilities for the purpose of generating *Makefiles* suitable for *make*.

For each directory in the X distribution, *imake* reads a bunch of configuration files that go to a lot of trouble to compensate for and adjust to the individual characteristics of your system. These are combined with an *Imakefile* from the current directory and the whole mess is sent through *cpp* to build a *Makefile* there. The same configuration files are used to build every *Makefile*; they are kept together in *mit/config*, isolating machine dependencies in one location to ease development and maintenance. In contrast, the *Imakefile* is directory-specific (it specifies the targets to be built in that directory) and is machine-independent.

This means that when the distribution is built on a different machine, only the configuration files need be changed. The *Imakefiles* do not need to be. If X configuration were specified directly using *Makefiles*, this would not be true. Because the configuration information in *Makefiles* is not portable, each one would have to be edited individually—bad enough for a single *Makefile*, but an overwhelming prospect for a project the size of X.

The value of being able to localize machine-dependent configuration information into one place should not be underestimated, particularly as X becomes more sophisticated and the number of systems on which it runs increases. The number of *make* variables used to build X has increased from approximately 60 in R1 to 100 in R3 to 150 in R4.

Another tool, *makedepend*, is used to generate header file dependencies for C source files in each directory after the *Makefile* has been built. Dependencies of targets upon object files can be statically listed in the *Imakefile*, but not those for header files. Different systems organize these differently so dependencies on them must be generated dynamically.

While X is being built, *imake* itself is located with the configuration files in *mit/config*. *makedepend* lives in *util/makedepend* if you use the compiled version (preferred), or in *util/scripts* if you use the shell script version (slower).

When X is installed, copies of *imake*, *makedepend*, and a related tool, *xmkmf*, are placed in a public directory, and the configuration files are copied to */usr/lib/X11/config*. *xmkmf* is used to bootstrap a *Makefile* from an *Imakefile* using the installed configuration files,[1] and can be used to build programs from outside of the X source tree, such as you might write yourself or get from comp.sources.x on Usenet.

The X configuration files make very few assumptions about the capabilities of *make* itself. Although several enhanced versions of *make* provide special features or extensions, any *Makefile* that relies on universal availability of those features will fail on systems with less-capable versions of *make*. *Makefiles* produced by *imake* in X do not use any of these constructs, so they should work with even the lowest-common-denominator *make* program.

Since *imake* passes its input through *cpp*, the configuration file writer can take advantage of several features not present in plain-vanilla *make*, such as parameterized macros (using #define), file inclusion (using #include) and conditional testing (using #if, #ifdef, #ifndef). This ameliorates much of the sobering effect of having to assume that a dumb *make* is the only one available.

Use of *cpp* can produce problems, too, of course:

(1) How to specify, in configuration files, comments that should end up in the *Makefile*. *make* comments are introduced by a ‘‘#’’ character—unfortunately, *cpp* treats lines beginning with ‘‘#’’ as preprocessor directives. A comment of the form

```
# if your system doesn't have ranlib, use /bin/true
```

is considered a symbol test by *cpp* (it begins with ‘‘if’’) and is gobbled up, while a comment like

```
# The next rule is a workaround for a broken compiler
```

is not understood by *cpp* and generates a syntax error. To get around this, comments should be preceded by ‘‘/**/#’’ instead of just ‘‘#’’. *cpp* will strip off the ‘‘/**/’’ (the empty C comment) and won't treat the line as a directive (though it's still liable to symbol substitution).

The exception to this ‘‘comment-commenting’’ convention is in the *Imakefile* itself, where *imake* automatically adds ‘‘/**/’’ onto lines beginning with ‘‘#’’ that are not preprocessor directives. The reason for this is to insulate the end user of *imake* (who simply writes the *Imakefile* and not the underlying configuration files) from the need to be aware of, or abide by, the commenting

[1] Normally a new *Makefile* is produced with ‘‘make Makefile’’, an operation that presumes you already have a properly configured *Makefile* containing the rules necessary to run *imake*. Hence the bootstrap problem. Obviously, if you know the proper incantation to utter, you can issue the appropriate *imake* command manually, but *xmkmf* eliminates the need. Besides, the only way you'd know which wand to wave is by having a reasonable understanding of *imake* in the first place—in which case you wouldn't be reading this!

convention. I would hazard a guess that many people are not aware of this exception, given the high incidence of *Imakefiles* containing ‘‘/**/#’’. Or maybe they’re just playing it safe.

Comments that are not to be passed through to the *Makefile* can be written as normal C comments (text surrounded by ‘‘/*’’ and ‘‘*/’’) and will be deleted by *cpp*.

(2) Sometimes the values of *cpp* symbols are to be concatenated. The symbol names cannot be concatenated in the configuration files, because that results in a different symbol name, so they must be kept apart somehow. The empty C comment is useful here, too. For instance, if Prefix, LibName and Suffix are defined as *lib*, *mylib* and *.a*, respectively, then to obtain the concatentation value *libmylib.a*, one writes Prefix/**/LibName/**/Suffix, not PrefixLibNameSuffix.

(3) The configuration files define several multiple-line macros that are intended to produce multiple lines of output. *cpp* joins multi-line macros into a single line, so some post-processing is necessary.

### 3. Configuration file architecture

To produce a *Makefile* from an *Imakefile*, the following configuration files are used:

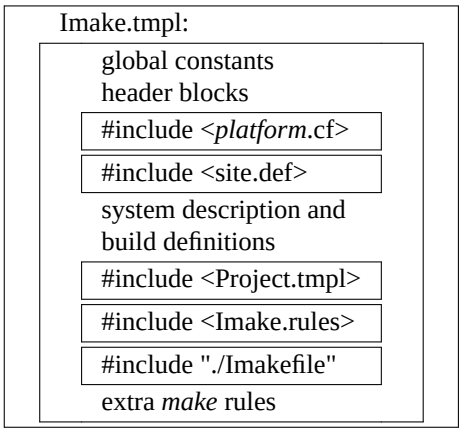
| | |
|---|---|
| Imake.tmpl | master template |
| *platform*.cf | platform-specific definitions (the filename varies) |
| site.def | site-specific definitions |
| Project.tmpl | X-specific default definitions |
| Imake.rules | *cpp* macros to generate *make* rules |

In general, if a port exists for your system, the only file that you should need to modify to build and install X is *site.def*. In some cases it may be necessary to modify the platform file. *Imake.tmpl*, *Project.tmpl* and *Imake.rules* should be left alone.[2]

If you are porting X to a new platform, you will need to write your own platform file, and modify the top part of *Imake.tmpl* slightly. If you are doing a new port of X, *or* porting the X configuration files for use with a different project, you are well-advised to study all of the configuration files thoroughly. Ignorance is not bliss in such instances.

*Imake.tmpl* contains an #include line for each of the other four configuration files and for the *Imakefile* in the current directory. It also contains in-line sections for global constant definitions, header block selection, description of system characteristics, build definitions, and extra *make* rules. The template is structured as follows to make everything fit together:

[2] It should be emphasized that ‘‘should be left alone’’ applies *only to building X itself*. If you are adapting the configuration files for use with another project, you will probably modify all of them somewhat. You may even find yourself in the position of having modified them to such an extent that they end up hacked to bits, incapable of configuring anything, with you left possessing only the faint hope that something useful will rise, Phoenix-like, from out of the remains. Be assured—it won’t. But cheer up; start again and learn from your mistakes.




Before these sections of the template are described, there are some general principles that should be kept in mind.

Much of the flexibility of the X configuration files is achieved through the use of the following construct, which defines *symbol* only if it has not already been defined:

```
#ifndef symbol
#define symbol value
#endif
```

This construct allows any system-, build- or project-related symbol to be given a default definition if none has been supplied earlier; coupled with support for inclusion of platform- and site-specific files prior to the section in which the default is defined, the default may be overridden by a definition occurring within those files. For example, the default for the C compiler symbol occurs in the build definitions section of *Imake.tmpl* and is defined thus:

```
#ifndef CcCmd
#define CcCmd cc
#endif
```

If the GNU C compiler is to be used instead, the default definition can be overridden simply by putting the following in *site.def*:

```
#ifndef CcCmd
#define CcCmd gcc
#endif
```

Use of the #ifndef/#endif construct is pervasive throughout the X11R4 configuration files, to a much greater extent than in the X11R3 files. This is especially true in the platform-specific files.

A fairly consistent pattern followed within the system/build section of *Imake.tmpl* and within *Project.tmpl* is that definitions for *cpp* symbols are listed first, followed by definitions for *make* variables. I presume this is done because there is greater flexibility available in defining *cpp* symbols, e.g., through #ifndef conditional testing. Since *cpp* can't tell whether a *make* variable has previously been defined, the strategy adopted is to associate a *cpp* symbol with a given *make* variable thus: define the symbol conditionally to allow the possibility of overriding, then equate the variable to whatever value the symbol finally ends up with. For instance, *Imake.tmpl* contains:

```
#ifndef CcCmd
#define CcCmd cc
#endif

. . .
CC = CcCmd
```

If no earlier definition of CcCmd overrides the default CC ends up as ‘‘cc’’. On the other hand, if the default is overridden to use ‘‘gcc’’ instead (e.g., in *site.def*), CC ends up as ‘‘gcc’’.

Sometimes ‘‘mixed’’ symbol definitions occur, in which *cpp* symbols are defined in terms of *make* variables. There are at least two reasons to do this.

(1) To avoid order-of-definition problems. Consider the two sets of definitions below.

```
#ifndef a
#define a b
#endif
A = a

. . .
#ifndef b
#define b z
#endif
B = b
```

```
#ifndef a
#define a ${B}
#endif
A = a

. . .
#ifndef b
#define b z
#endif
B = b
```

What ends up in the *Makefile* is:

```
A = b
B = z
```

```
A = ${B}
B = z
```

The example on the left is dependent on the order in which a and b are defined. As written above, a (and hence A) is defined in terms of a symbol that hasn’t been defined yet; both get the value of a literal ‘‘b’’. The example on the right works; b is defined as ‘‘z’’, B gets the same value. A becomes ‘‘${B}’’, which is not evaluated further until *make* is run. At that time A evaluates properly to ‘‘z’’, independently of the order in which a and b are defined in the configuration file.

(2) To allow for greater flexibility at installation time. For example, UsrLibDir and USRLIBDIR are defined as:

```
#ifndef UsrLibDir
#define UsrLibDir $(DESTDIR)/usr/lib
#endif

. . .
USRLIBDIR = UsrLibDir
```

Symbols such as IncRoot, BinDir, AdmDir are defined similarly, even though DestDir (to which DESTDIR is eventually equated) is defined earlier (i.e., there is no order-of-definition problem here). If a user wants the install to take place under a different root than DestDir, the command ‘‘make DESTDIR=/alternate/root install’’ suffices, by overriding the definition of DESTDIR in the *Makefile*. If UsrLibDir, etc., were defined directly in terms of DestDir rather than DESTDIR, this would not be possible.[3]

[3] Actually, it would be possible, but cumbersome: ‘‘make install BINDIR=/alt/bin/dir ADMDIR=/alt/adm/dir LIB-

Each of the sections of *Imake.tmpl* is described below. The descriptions only contain representative examples of the symbols used in each configuration file. For exhaustive (and exhausting?) lists, consult the appendix.

### 3.1. Global constant definitions

This section of *Imake.tmpl* defines two symbols (YES as 1 and NO as 0). References to these symbols are legion throughout the rest of the configuration specification and their values should not be changed.

Note: symbols #define'd in the configuration files or the *Imakefile* have nothing to do with symbols #define'd in your programs; they are entirely independent.[4]

### 3.2. Header block selection

The header block section of *Imake.tmpl* determines the type of machine on which *imake* is being run. This is done by looking for a *trigger*—a *cpp* preprocessor symbol that uniquely and unambiguously indicates a given platform. For instance, ''sun'' is defined only on Sun systems, ''apollo'' only on Apollo systems, and ''ultrix'' is only on Ultrix systems. Their header blocks look like this:

```
Sun:
#ifdef sun
#define MacroIncludeFile <sun.cf>
#define MacroFile sun.cf
#undef sun
#define SunArchitecture
#endif /* sun */

Apollo:
#ifdef apollo
#define MacroIncludeFile <apollo.cf>
#define MacroFile apollo.cf
#undef apollo
#define ApolloArchitecture
#endif /* apollo */
```

```
Ultrix:
#ifdef ultrix
#define MacroIncludeFile <ultrix.cf>
#define MacroFile ultrix.cf
#ifdef vax
#undef vax
#define VaxArchitecture
#endif
#ifdef mips
#undef mips
#define MipsArchitecture
#endif
#undef ultrix
#define UltrixArchitecture
#endif
```

The trigger symbol might be predefined by *cpp* itself; if no such predefined symbol is available,[5] *imake* is built to explicitly pass a definition for one to *cpp* to cause the correct header block to be selected.

If no trigger is defined, a generic configuration header block is selected instead. Since that means the platform wasn't properly determined, a warning is written into the *Makefile*:

```
# WARNING:  Imake.tmpl not configured; guessing at definitions!!!
# This might mean that BOOTSTRAPCFLAGS wasn't set when building imake.
```

The resulting generically-configured *Makefile* will probably fail in one of a number of strange and wondrous ways when used to try to build anything, With any luck, the hapless user will examine the *Makefile* to figure out what went wrong, see the warning, and realize that the platform wasn't determined properly.

Failure to find a defined trigger symbol might occur because no port exists for the machine in question, and thus no header block for it exists. (Each time X is ported, a header block for the new platform is added to

---

DIR=/alt/lib/dir ...''

[4] I once wondered about this, which seems pretty ridiculous now.

[5] A goal ANSI C will help us reach?

this section of *Imake.tmpl*.) Failure will also occur if *imake* was not built properly (i.e., it doesn't pass the proper trigger symbol definition to *cpp*).

Assuming the proper header block is selected, several things happen inside of it. The name of the associated platform-specific *.cf* file is defined for later inclusion by the template; one or more architecture indicator symbols are defined that can be used by later configuration sections to test for particular software or hardware platforms; the trigger symbol is undefined so it won't trigger any other header block.

A header block may define a single architecture indicator symbol that refers both to the software and hardware, or separate indicators may be defined for an operating system that runs on multiple hardware types, For example, ''ultrix'' indicates an Ultrix system, but it is no longer true (as it once was) that a VAX can be assumed as the hardware platform—RISC Ultrix systems run on Mips chips now. Thus UltrixArchitecture is defined as the software indicator symbol, and VaxArchitecture or MipsArchitecture as the hardware indicator.

Software indicator symbols should be unique. Hardware indicator symbols are not necessarily unique in themselves and may be shared across different software platforms, e.g., MipsArchitecture can be defined for Ultrix or SGI systems, VaxArchitecture for Ultrix or BSD systems.

*Imakefiles* should always test for indicator symbols rather than trigger symbols, since the former are more reliable (the latter are undefined by the header block, anyway).

The use of predefined or *imake*-supplied *cpp* trigger symbols is fraught with peril, and one must construct the header blocks carefully, for two reasons:

(1) Symbols may be ambiguous. For instance, ''vax'' can be defined both under Ultrix and under BSD. Thus, the Ultrix header block must come first. It tests for ''ultrix'' and if that succeeds, defines UltrixArchitecture and undefines ''vax'' so the BSD block will fail.

(2) Symbols may become ambiguous in unanticipated ways in the future. The ''mips'' symbol is an example. In R3 it was used to unambiguously detect a true Mips machine. This can no longer be done given the presence of that symbol on other systems that also run on Mips chips now.

When porting X to other platforms, it is sometimes difficult to know either what the trigger symbol should be, or what indicator symbol(s) to use. Consider Mips machines running RISC/os. ''mips'' is insufficient as a trigger, because it is does not unambiguously define Mips systems. The hardware indicator symbol is clear enough, MipsArchitecture, but the software indicator is less so. The name of the Mips operating system suggests that RiscosArchitecture might be a good choice. Guess again. Acorn Computers Ltd. has an OS with a similar name, ''RISC OS''.[6]

### 3.3. platform.cf

After the header block section has determined which platform-specific file to use, that file is then included by *Imake.tmpl*:

```
/**/#########################################################################
/**/# platform-specific configuration parameters - edit MacroFile to change
#include MacroIncludeFile
```

where MacroIncludeFile has been defined properly by the header block.

---

[6] My own preference is to use ''mipsriscos'' as the trigger symbol, MipsArchitecture as the hardware indicator, and MipsRiscosArchitecture (which seems suitably unique, but has the disadvantage of being quite long, and uneuphonious to boot) as the software indicator. Mips Computers now claims to consider ''RISCOS'' their trigger symbol.

The platform file contains definitions needed to make X build and install correctly on a particular system. Some common things found here are definitions for the operating system name version numbers (major and minor), C compiler version numbers, and workarounds for missing commands. Overall, these files are really quite a hodgepodge of different things, and it is difficult to describe them in any general way. You should read through the *.cf* file for your system before you try to compile anything, to get an idea what can be done with it. (It doesn't hurt to read *all* the *.cf* files, as a matter of fact, especially if you are doing a new port.)

It is important that version numbers in the platform file accurately reflect your system. Some symbol values are contingent upon the OS or C compiler version, to accommodate system changes, deficiencies, or bugs, so you want to make sure you get the right definitions. For example, *sun.cf* contains the following:

```
#if OSMajorVersion <= 3
#define HasVoidSignalReturn NO
#else
#define HasVoidSignalReturn YES
#endif
```

This reflects a change in the return type of the *signal()* system call (from *int** to *void**) on Sun systems beginning with SunOS 4.0. Ultrix systems underwent a similar change between 3.0 and 3.1 but the *ultrix.cf* file doesn't yet reflect the change. Perhaps in R5.

A common missing-command workaround occurs for those systems that have no BSD-compatible *install* command, such as in *cray.cf*:

```
#define InstallCmd sh $(SCRIPTSRC)/install.sh
```

It is important to set the value of the SystemV symbol to either YES or NO in the platform file because (1) the value of parameters defined in *site.def* may depend on it, and the default value is not set until the system/build definition section (i.e., after *site.def*); (2) the default values of many parameters in later parts of the template depend on the value of SystemV. For instance, there is usually no *ranlib* under System V, so the default is defined as a no-op:

```
#ifndef RanlibCmd
#if SystemV
#define RanlibCmd /bin/true
#else
#define RanlibCmd ranlib
#endif
#endif
```

It is perhaps safer to rely on predefined *cpp* symbols (should they exist) in the platform-specific files than in the header block section of *Imake.tmpl*. Once you know the system type, the universe of such symbols is more constrained and their meanings cannot clash with those of symbols defined on other vendors' systems.

The platform files were significantly reorganized between R3 and R4. In R3, each platform file was expected to contain a definition for each of the build parameters (C compiler, loader, link command, etc.). Whenever a change was made that affected one platform file, such as defining a new symbol, it usually affected them all. Since the definitions were usually broadly similar across platforms, this was more work than necessary. The approach adopted in R4 is to supply best-guess values as the default definitions for these parameters in the template, which individual platform files may override as necessary. This set of defaults defines a baseline configuration. There are two advantages to this method. The platform files need only specify where they *differ* from the baseline, by overriding the default definitions; and changes or

additions to the baseline usually require retrofitting of only a few platform files.

### 3.4. site.def

This file contains site-specific definitions. It is used to reflect local site-wide conventions. Should you wish to override the defaults, this is the place to indicate installation directories, whether to build the server and example programs, any special versions of programs to use during the build, etc.

The name *site.def* seems, to me at least, something of a misnomer. For instance, one of the things that is likely to be set there is HasLargeTmp, to indicate whether you have a large temp file directory. *site.def* is the place for this definition (it depends on your particular file system layout, so it doesn't go in the platform-specific file, and it's not *cpp*-guessable, so it doesn't go in the system description section). But a ''site'' can be a location at which multiple hosts are administered, and which may be configured dissimilarly. Some may have a large temp directory, others may not. Similarly, *site.def* is the logical place to specify that you want to use the GNU C compiler, but you might not necessarily want or be able to use it on all hosts at a site. In some ways *host.def* might be a better name for this file.

### 3.5. System description and build parameter definitions

The platform- and site-specific files are followed by a section containing a number of default definitions. Generally, these describe system characteristics (e.g., does it have *vfork()*? does it have sockets?) or are related to management of the build and installation[7] processes (e.g., what is the name of the C compiler? does the loader need any special flags?). These are not X issues and so are segregated into their own section.

This section of *Imake.tmpl* should not be modified; definitions should be overridden in platform- or site-specific files.

### 3.6. Project.tmpl

This file contains definitions for parameters that are specific to X. Examples: whether to build debuggable versions of libraries, the screen resolution, what sorts of connections to accept (UNIX, TCP or DECnet sockets), where programs or libraries should be installed.

A number of the *make* variables in *Project.tmpl* are directory locations. Most of them are named in one of two ways, *XXX*DIR or *XXX*SRC. Variables of the first form are defined by equating them to the values *cpp* symbols having similar names (e.g., LIBDIR=LibDir). The *cpp* symbols are defined in the usual default-provided-but-override-allowed manner, because they refer to where things are to be placed at installation time, something which the installer might wish to change.

Variables of the second form, *XXX*SRC, indicate the layout of various source directories within the X distribution. They are not supposed to be changed, so there is no provision for overriding them; rather than being equated to *cpp* symbols, they are simply defined directly (e.g., SERVERSRC=$(TOP)/server).

*Project.tmpl* should not be modified; definitions should be overridden in platform- or site-specific files.

### 3.7. Imake.rules

This file contains *cpp* macros used to generate *make* rules from concise descriptions of targets and dependencies. Since all the *cpp* symbol substitution functionality is available, these macros can be very powerful and are sometimes quite complicated.

**Example:** A simple rule to compile a program might be (this isn't an actual X rule):

---

[7] Viz., *how* things should be installed, not *where*; defaults for the latter are in *Project.tmpl*.

```
# define CompileProgram(program,objects,libraries)
program: objects
        cc −o program objects libraries
```

If invoked in an *Imakefile* as ‘‘CompileProgram (xproga, main.o, −lm)’’, this macro would expand in the *Makefile* to:

```
xproga: main.o
        cc −o xproga main.o −lm
```

The invocation ‘‘CompileProgram (xprogb, main.o parse.o scan.o, −lm −ll −ly)’’ would expand to:

```
xproga: main.o parse.o scan.o
        cc −o xproga main.o parse.o scan.o −lm −ll −ly
```

There is one problem with these examples, which is that the CompileProgram() macro definition spans multiple lines, and *cpp* macros are normally single-line. One can can write multiple-line macros by putting a ‘‘\’’ at the end of lines to be continued onto the next, but this doesn’t work for *imake’s* purpose since *cpp* will still collapse the expanded macro onto a single (possibly quite long) line. That makes *make* quite unhappy.

To get around this, special place-markers ‘‘@@\’’ are used on all lines of a rule definition but the last. The ‘‘\’’ allows multiple-line definitions for *cpp* and the ‘‘@@’’ lets *imake* know how to postprocess *cpp* output in order to split expanded macros back up into multiple lines suitable for *make*. This keeps both *cpp* and *make* happy.

So: the *real* way to write the CompileProgram() macro is:

```
# define CompileProgram(program,objects,libraries)                @@\
program: objects                                                  @@\
        cc −o program objects libraries
```

The invocation ‘‘CompileProgram (xproga, main.o, −lm)’’ comes out of *cpp* looking like this:

```
xproga: main.o @@ cc −o xproga main.o −lm
```

*imake* sees the ‘‘@@’’ and knows it has to break the line back up to produce the intended result.

In short, the purpose of the strange syntax is to keep *cpp* from totally destroying the usefulness of the output for *make* purposes. Thus, although *imake* relies heavily on *cpp*, one of *imake*’s jobs is to undo some of the damage done by *cpp*—an uneasy alliance indeed.[8]

All the macro definitions in *Imake.rules* are embedded within the standard override construct:

```
#ifndef rulename
#define rulename . . .
#endif
```

[8] This use of ‘‘@@\’’ seems quite natural and obvious, now that I have worked with it for a while. I remember, though, that the first several times I looked at the X *imake* rules, especially the more complicated ones, the formatting and syntax seemed so bizarre to me that a switch flipped in my brain, which then refused to comprehend anything at all. Repeated exposure gradually inured me to the infelicities of the syntax.

Thus, even rules are subject to being overridden. If a rule needs to be overridden for a particular platform, the default definition will be placed in the platform-specific file (*sgi.cf* does this). If a rule only needs redefining in a single directory, it may be better to undefine it and redefine right it in that directory's *Imakefile* (see *mit/config/Imakefile* for an example).

In R3, rule definitions were not bracketed within #ifndef/#endif pairs. This meant that although individual *Imakefiles* could undefine and redefine rules, platform files could not. Any definition of a rule in a platform file would be unconditionally replaced by the definition in *Imake.rules* (possibly accompanied by a ''symbol redefined'' message). The R4 architecture is an improvement because it is not constrained by this limitation.

*Imake.rules* should not be modified; definitions should be overridden in platform- or site-specific files.

### 3.8. ./Imakefile

The last file included by the template is the *Imakefile* from the current directory. As already mentioned, *imake* causes *make* comments in this file to be made *cpp*-safe by preceding them with ''/**/'' if necessary. The *Imakefile* indicates targets to be built and installed, and their dependencies, in terms of the *cpp* macros defined in *Imake.rules*. There may also be targets for generating dependencies, creating installation directories, creating lint libraries or tag files, etc.

*Imakefile*-writing is described in more detail in a later section.

### 3.9. Extra make rules

The last section of *Imake.tmpl* adds some common targets to the *Makefile*, such as a ''Makefile'' target to regenerate the *Makefile* itself, and default ''tags'' and ''clean'' targets. If the directory has subdirectories, rules to recurse through them for the ''install'', ''install.man'', ''clean'', ''tags'', ''Makefiles'' and ''includes'' targets are generated. These rules are added if the *cpp* symbol IHaveSubdirs is defined and the *make* variable SUBDIRS is equated to the list of subdirectories in the *Imakefile*. (Note: you may get syntax error messages from *make* if SUBDIRS is defined to be empty, i.e., ''SUBDIRS =''. Another possibility is that when you make any of the directory-traversing targets, *make* will loop forever.)

## 4. Building imake

Before you can build any part of X, *imake* itself must be built. ''make World'' in the top-level X source directory builds *imake* automatically (in *mit/config*). The following discussion explains what happens during that process and some of the things you may need to do in preparation for the ''World'' build. You want to read this section if ''make World'' dies when you try it, or you want to port X to another platform, or you want to build *imake* or adapt the X configuration files for use with other projects. You need to look at the following files: *Makefile.ini*; *imakemdep.h*; *Imake.tmpl*.

### 4.1. Determine what you are trying to accomplish

When you build *imake* you want to (1) get it to compile successfully, so you can use it; (2) guarantee that it makes sure *cpp* knows the trigger symbol—whether *cpp* predefines it or not—so you don't end up with generically-configured *Makefiles*.

Conspiring to keep you from your goal are three dilemmas.

- *imake* generates *Makefiles* for use in compiling programs; *imake* is compiled using a *Makefile*.
- *imake* needs to know your system's trigger symbol in order to pass it along to *cpp* for proper *Makefile* generation. But *imake* doesn't know the trigger itself if neither *cc* nor *cpp* predefine it.
- Some systems require special C compiler flags to ensure proper compilation of all but the most trivial programs. One of the facilities provided by *imake*-generated *Makefiles* is that these flags can be automatically specified. Of course, that is small consolation when the program to be compiled is *imake*—

which happens to be just such a non-trivial program itself.

The solution to the first dilemma is to use a minimal handwritten file *Makefile.ini* instead.[9] You might need to hack *Makefile.ini* file slightly for your system—but only slightly. If you find yourself making large changes, that is a symptom you don't understand what you're supposed to be doing. Stop and think some more first.

The second and third dilemmas are solved by manually passing the trigger symbol to the *imake* compilation and using a small bootstrap program that knows what special flags are necessary to get *imake* to compile without error. The commands in *Makefile.ini* that build *imake* look something like this:[10]

```
$(CC) -o ccimake $(BOOTSTRAPCFLAGS) ccimake.c
$(CC) -o imake $(BOOTSTRAPCFLAGS) imake.c `./ccimake`
```

BOOTSTRAPCFLAGS is the *make* variable used to pass the trigger value for your system to *ccimake* and *imake* so they know the platform type. It is typically ''BOOTSTRAPCFLAGS=-D*trigger*'' if your *cpp* doesn't predefine the trigger, empty otherwise.[11]

*ccimake* is the bootstrap program. It is—by design—so simple that it should compile without any special treatment, and is used to figure out any extra flags needed on your platform to get *imake* to compile. BOOTSTRAPCFLAGS is supplied to the *ccimake* build so it can select the necessary flags by platform type.

*imake* is built using the flags supplied by *ccimake*, so that it compiles correctly, and with the the trigger value supplied in BOOTSTRAPCFLAGS, so that it learns and memorizes the trigger for itself. When *imake* passes the trigger to *cpp*, that in turn allows *cpp* to properly determine the correct header block in *Imake.tmpl* when *Makefiles* are generated.

### 4.2. Lay the groundwork

First, you need to determine what the trigger symbol is for your system, as well as the value of BOOTSTRAPCFLAGS. For existing ports, you can find out the trigger symbol by examining the header block section of *Imake.tmpl*. The value of BOOTSTRAPCFLAGS can be found in the platform *.cf* file for your system. Look for a line that defines BootstrapCFlags; if *cpp* predefines the trigger, there will likely be no such line. This means BOOTSTRAPCFLAGS is empty. Otherwise the value will probably be ''-D*trigger*'', e.g., ''-DmacII'', ''-Datt'', ''-Daix''.

A complication likely to arise in the future with regard to predefined preprocessor symbols is that ANSI C takes a dim view of all (or almost all) such. BOOTSTRAPCFLAGS is likely to be non-empty on more systems as implementations of ANSI C become distributed more widely.

For new ports, you need to invent a trigger symbol that uniquely identifies your system and define BOOTSTRAPCFLAGS accordingly. Suppose you have a Brand X system. You can use ''brandx'' as the trigger and ''BOOTSTRAPCFLAGS=-Dbrandx''. You also must modify *imakemdep.h*. This header file is #include'd by three programs, *ccimake*, *imake* and *makedepend*, and has one section for each.[12]

---

[9] There might be a *Makefile* in *mit/config* already, but it might not have been generated on your system, so it's not safe to use. It has a bug in it, anyway.

[10] The commands actually use CFLAGS, which includes BOOTSTRAPCFLAGS as part of its definition; you get the idea.

[11] The BOOTSTRAPCFLAGS mechanism was not present in some earlier versions of X11 (e.g., R1). The number of systems to which X had been ported then was smaller and all of them had *cpp*'s that predefined a unique symbol.

[12] *Imake.tmpl* contains some comments near its beginning pertaining to new ports. These indicate that *imake.c*, and *main.c* in the *makedepend* source, should be modified. These comments are correct for R3 but evidently were not updated for R4; in both cases the modifications should be made to *imakemdep.h*. Similarly, the *README* file refers to *ccflags.c*, the R3 equivalent of *ccimake.c*.

#### 4.2.1. imakemdep.h: ccimake section

The first section of *imakemdep.h* pertains to *ccimake*; it consists of a bunch of #ifdef/#endif blocks that define *imake_ccflags* according to the trigger symbol. *imake_ccflags* is defined as the flags needed to compile *imake* on your platform. For instance, if your Brand X system is System V-based, you need to specify that:

```
#ifdef brandx
#define imake_ccflags "−DSYSV"
#endif
```

*ccimake* simply writes the value of *imake_ccflags* to its standard output. Common flags in this definition are −DSYSV or −DUSG to indicate System V or USG systems. Other flags might also be necessary—see the ‘‘hpux’’ and ‘‘umips’’ blocks for some particularly unpleasant examples. If no special flags are necessary to compile *imake* (e.g., under Ultrix or BSD), there need not be any block for your system, and *ccimake* simply writes out a default definition of *imake_ccflags* (currently −O).

You might have to fool around trying to compile *imake* by hand to determine the correct flags before you know how to define *imake_ccflags*.

#### 4.2.2. imakemdep.h: imake section

The second section of *imakemdep.h* is for *imake*. It too has a set of #ifdef/#endif blocks, this time to select a trigger symbol definition based on the trigger symbol. That sounds circular and it is. These blocks add entries to *cpp_argv*[ ], which is an array of strings to be passed to *cpp* by *imake*. If your *cpp* predefines the correct trigger symbol automatically, *imake* doesn’t need to pass a definition for it to *cpp*, and you can leave *cpp_argv[ ]* alone. Otherwise, this is where you want your trigger symbol to be listed, and there should be a block something like this:

```
#ifdef brandx
    "−Dbrandx",     /* for Brand X systems */
#endif
```

This causes *imake* to pass ‘‘−Dbrandx’’ to *cpp* to make it simulate predefinition of the trigger. Then, when *cpp* reads the configuration files, the trigger will have been defined and the correct header block in *Imake.tmpl* will be selected.

#### 4.2.3. imakemdep.h: makedepend section

Following the sections for *ccimake* and *imake*, *imakemdep.h* contains a third section for *makedepend* (the compiled version; if you use the shell script version of *makedepend* in *mit/util/scripts*, this section of *imakemdep.h* is irrelevant). It looks for various system- and compiler-related definitions that may be predefined by *cc* and/or *cpp*. *makedepend* uses this information to be smart. Consider the following C program fragment:

```
#ifdef ultrix
#include "inca.h"
#else
#include "incb.h"
#endif /* ultrix */
```

If ‘‘ultrix’’ is defined, *makedepend* knows to generate a dependency for ‘‘inca.h’’ and not ‘‘incb.h’’; a dumb *makedepend* has to to assume dependencies on both.

It's easiest simply to leave this section of *imakemdep.h* alone.

### 4.3. Build the program

You're ready to begin (this should actually be simple if you've done the above correctly). The first thing to do is ensure the absence of any detritus that might be laying around from previous builds:

```
make −f Makefile.ini clean
```

Then build *ccimake* and *imake*. If your *cpp* predefines the trigger, you can build with:

```
make −f Makefile.ini
or
make −f Makefile.ini BOOTSTRAPCFLAGS=
```

Otherwise, specify the trigger explicitly. E.g., for Brand X, use:

```
make −f Makefile.ini BOOTSTRAPCFLAGS=−Dbrandx
```

### 4.4. Test your handiwork

If *imake* is supposed to pass a trigger symbol definition to *cpp*, you should test whether it actually does or not by executing the following command (**−T***/dev/null* provides an empty input template, and **−s***/dev/null* throws away the output so it doesn't clobber the *Makefile* in your current directory):

```
imake −v −T/dev/null −s/dev/null
```

**−v** causes *imake* to print out the *cpp* command that it executes. If you don't see a −D*trigger* in that command, *imake* wasn't built properly.

### 4.5. Modifying other configuration files for a new platform

If you are developing a new port to another system, you have to do more than be able to compile *imake*, you also need to be able to use it in conjunction with the configuration files. First, you must modify the header block section of *Imake.tmpl* to add a block for your system.[13] It will look something like this:

```
#ifdef brandx
#define MacroIncludeFile <brandx.cf>
#define MacroFile brandx.cf
#undef brandx
#define BrandxArchitecture
#endif /* brandx */
```

Second, you must create a platform-specific file *brandx.cf*. If your bootstrap flags are non-empty, this should contain, at minimum, ''#define BootstrapCFlags −Dbrandx''. Most certainly it will have other things in it, too. You will discover just what as you go through the process of porting the server and/or clients.

---

[13] This is the only section of *Imake.tmpl* that should ever need modifying. Other changes would be specified in the platform-specific file for your system.

## 5. Building X Itself, or, When Is BOOTSTRAPCFLAGS Necessary?

The question addressed in this section is: how can you get all the *Makefiles* built? (This document is really about configuring X, not building it, and configuring X amounts to building the *Makefiles*.)

If you have gotten this far, you know you can get *imake* compiled and can proceed to build X itself. *Before you do anything else*, copy the top-level *Makefile* somewhere safe (outside of the source tree, for instance). If something goes wrong and this file gets trashed, you want to be able to recover it. You're asking for trouble if you don't.

The ''do everything'' operation in building X is ''make World''. It builds *imake*, generates all the *Makefiles*, removes extraneous object files, builds the header file tree, generates dependencies, and compiles everything. The interesting parts of this from a configuration perspective are the first two steps.

The important questions are: (1) what is the value of BOOTSTRAPCFLAGS; and (2) when must BOOTSTRAPCFLAGS be specified? You have already answered the first question by determining how to build *imake*, so only the second is considered below.

When you build *imake* ''manually'' in *mit/config* using *Makefile.ini*, you must specify BOOTSTRAPCFLAGS explicitly if it is non-empty. What about when building from the top-level directory, e.g., ''make World''? It is a useful exercise to build *imake* by hand to test your understanding of the issues involved, but that won't help you to do the ''World'' build—the first thing ''make World'' does, unfortunately, is throw *imake* away and rebuild it from scratch.

The X Release Notes (section 2) indicate that for ''make World'' you should specify BOOTSTRAPCFLAGS if you find a definition for BootstrapCFlags in the platform *.cf* file (which means there is no unique predefined *cpp* symbol). By implication, you do not need to specify BOOTSTRAPCFLAGS if there is no value of BootstrapCFlags in your platform *.cf* file. This means essentially that the empty value ''BOOTSTRAPCFLAGS='' is sufficient to compile *imake* properly, which brings up a subtle point. Each *Makefile* created in the X distribution contains a line that says

BOOTSTRAPCFLAGS=*something*

where *something* is the default value for any *make* operation, and may or may not be empty. Thus, when you execute a command such as

make World BOOTSTRAPCFLAGS=−Dbrandx

you are not just *providing* a value of BOOTSTRAPCFLAGS, you are also *overriding* the default value in the *Makefile*.

The Release Notes do not cover the general case. Rather, they assume you are working from a virgin distribution (where the default BOOTSTRAPCFLAGS is in fact empty).

The implication that BOOTSTRAPCFLAGS need not be specified if BootstrapCFlags is not defined involves a hidden assumption, i.e., that BOOTSTRAPCFLAGS *already* has the empty value in the top-level *Makefile*. If you obtain your X distribution from a system on which X has been built and on which BOOTSTRAPCFLAGS is non-empty, that assumption is incorrect. If you don't explicitly provide an empty value by saying

make World BOOTSTRAPCFLAGS=

to override the default in the *Makefile*, a value which is incorrect for your system is used instead. You can get some very strange results this way.

I submit that, in the general case, the deciding factor determining whether to specify BOOTSTRAPCFLAGS is not so much whether BootstrapCFlags is defined or not, but whether you've built your *Makefiles* yet. If you have, you don't need to specify BOOTSTRAPCFLAGS. If you haven't, you do need to specify it, *even if it's empty.* In practice, this means that when you are building X on a new system, you should assume BOOTSTRAPCFLAGS in the *Makefile* is incorrect and explicitly override it on the command line. Normally this will be on the first ''World'' build.

Once the *Makefiles* have been built properly, BOOTSTRAPCFLAGS will be defined correctly in them and they will take care of propagating the right value for you automatically—forever, even into another operation that rebuilds *imake* and the *Makefiles*. You can test this for yourself on a machine that has a non-empty BOOTSTRAPCFLAGS. Do ''make World BOOTSTRAPCFLAGS=−D*trigger*'', then ''make World''. You'll see during the latter ''make World'' that a non-empty BOOTSTRAPCFLAGS is supplied for you.

**Note 1:** It is not necessary to do a ''World'' build to build *imake* and the *Makefiles*; there is a ''mastermakefiles'' target that will do so. This is a far more modest undertaking than doing the ''World'' build and you can check a few of the *Makefiles* afterward to see if the ''World'' build is likely to succeed or not: If BOOTSTRAPCFLAGS is correct and the correct header block was selected, you can then go ahead and do ''make World'' without having to specify BOOTSTRAPCFLAGS.

**Note 2:** The above discussion assumes that if BOOTSTRAPCFLAGS is not empty, BootstrapCFlags is defined correctly in the platform *.cf* file, because BOOTSTRAPCFLAGS normally gets its value from that symbol when *Makefiles* are generated.

### 5.1. Alternatives ways of specifying BOOTSTRAPCFLAGS

There are two alternatives to specifying BOOTSTRAPCFLAGS explicitly on the command line when you build the *Makefiles*, both sneaky and both deprecated. They are mentioned here only to point out why you shouldn't use them.

First, you can edit *config/Makefile.ini* manually to set the value of BOOTSTRAPCFLAGS directly. The reason this is not a good idea is that if you give your X distribution to somebody else that doesn't have the same kind of machine, you've given away an explicitly misconfigured *Makefile.ini*. The recipient might fail to appreciate the subtle irony of this gesture.

Second, you can forget about BOOTSTRAPCFLAGS entirely. That's correct—you can build *imake* with wild abandon and total lack of regard for whether it knows about the correct trigger symbol or not. ''But on some systems, *imake* must pass the trigger definition to *cpp* explicitly,'' you say, ''and *imake* compiled in such a reckless and irresponsible manner may not do the job.'' Quite right. However... *imake* also examines your environment, and the value of IMAKEINCLUDE, if defined there, is passed to *cpp*. The value of IMAKEINCLUDE must begin with ''−I'' (or *imake* will reject it), but you can be clever and set it to ''−I. −Dbrandx'' and the trigger symbol will be passed through to *cpp* and your *Makefiles* will build happily.

This is sneaky because it uses the environment to affect the build process in a way that is not evident. Worse yet, if you actually install an *imake* built this way, it won't work for anyone else who isn't privy to the IMAKEINCLUDE convention.

(A third alternative is to edit BOOTSTRAPCFLAGS to the proper value in the top-level *Makefile* before ''make World''. This is functionally equivalent to specifying it on the command line.)

## 6. Imakeconoclasm—X configuration bugs

Given the size of the X project, there are astonishingly few outright errors in the files involved in the configuration process. The ones I know about are listed below.

(1) Assume the following scenario. You do a ''make World''. This builds *imake*, generates all the *Makefiles*, cleans all the directories and finally builds the X stuff.

Then you decide to do some work in an X directory that requires the *Makefile* to be rebuilt, so you execute ''make Makefile''. What happens? You might be surprised to observe that *imake* is rebuilt before your *Makefile* is regenerated. This reason is that the rules for the ''Makefile'' target check whether *imake* exists, and if not they build it first. Since the ''World'' build does a clean after generating the *Makefiles*, *imake* was thrown away.

In fact, *imake* will be built the first time ''make Makefile'' is done after the ''World'' build even if the cleaning had not been done. The rules that check for *imake*'s existence look for *Makefile* in *mit/config/Makefile.ini* and build *imake* with that if it's there. (It will be because the ''World'' build generates it.) But the dependency for *imake* in *Makefile* is *imake.o* while the dependency in *Makefile.ini* (with which *imake* is initially built) is *imake.c*. This means that even if *imake* is present in *mit/config*, it will be built again because *imake.o* isn't.

That isn't actually a bug, but it can be confusing. Possibly even more confusing than the previous paragraph.

What *is* a bug is that *Makefile* in *mit/config* does not pass BOOTSTRAPCFLAGS to the *imake* compilation. If your BOOTSTRAPCFLAGS is non-empty, you end up with a bad *imake*. The fix is as follows:

Change:
```
DEFINES = $(SIGNAL_DEFINES)
```
To:
```
DEFINES = $(SIGNAL_DEFINES) $(BOOTSTRAPCFLAGS)
```

(2) The IncRoot symbol is defined twice, once in *Imake.tmpl* and once in *Project.tmpl*. This is a minor nit, since it gets the same value in both files. However, if you use a copy of the configuration files as a base for your own projects, you will probably modify these files and you should be aware that changing the definition in *Project.tmpl* has no effect if you leave both definitions in.

(3) *makedepend* has some hardwired pathnames in the source code. This may bite you if you use *gcc* or compile on a Mips system under the BSD environment, or on other systems that put include files somewhere other than */usr/include*.

(4) The default value of ExecableScripts in *Imake.tmpl* should be given a value based on SystemV, not SYSV.

Change:
```
#ifndef ExecableScripts
#ifdef SYSV
```
To:
```
#ifndef ExecableScripts
#ifdef SystemV
```

This is simply for consistency. Probably the right thing always happens anyway.

**7. Writing Imakefiles.**

This section assumes that *imake*, *makedepend* and *xmkmf* are installed in a public directory somewhere, which implies the additional assumption that the configuration files are installed where *xmkmf* can get at them. Why make these assumptions?

Normally you work with both an *Imakefile* and a *Makefile* and after you make changes to the *Imakefile*, you do ''make Makefile'' to regenerate the *Makefile*. You can use *Makefile* to rebuild itself this way because it has a ''Makefile'' target in it containing rules to do so.

That doesn't work when you're starting from scratch with just *Imakefile*. This is where *xmkmf* comes in useful. Once you've written your *Imakefile*, you just execute ''xmkmf'' and it will bootstrap a *Makefile* for you. Thereafter you can use ''make Makefile'' when *Makefile* needs rebuilding.

However, in order to bootstrap *Makefile*, you need *xmkmf*. *xmkmf* assumes *imake* has been built and installed. If an installed *imake* is used, an installed *makedepend* must also be used.[14] And if installed versions of *xmkmf*, *imake* and *makedepend* are used, they will expect the configuration files to have been installed. Hence the assumptions at the beginning of this section.

Here is the simplest *Imakefile*:

```
← that's it right over there
```

That was easy. But what can you do with it? Quite a lot, actually. First, execute ''xmkmf'', and you'll have the corresponding *Makefile*. Then you can try a few operations such as ''make clean'', ''make'', ''make Makefile'' to see what happens. (Note in particular that last target and observe that you can start with no *Imakefile* and get your first *Makefile* with ''touch Imakefile ; xmkmf''.)

Since *make* actually executes correctly for all of those targets (or should, anyway), you can see that a lot of structure is supplied for you by the configuration files. If you look at the *Makefile* generated from the null *Imakefile*, you might be surprised by the complexity of it. Perhaps even alarmed.

To test the syntactic correctness of your *Imakefile*:

```
xmkmf ; make Makefile
```

This builds *Makefile* twice. *xmkmf* builds the *Makefile* from scratch and fails if the configuration is bad. *make* builds a new *Makefile* using the one built by *xmkmf*, and fails if that one is not legal. This can happen due to misspelled rule names, missing ''@@\'' markers, leading spaces in the command portions of rules instead of tabs, etc. None of these should occur with a null *Imakefile*, but any may very well happen when you're writing real ones.

### 7.1. Some commonly-used rules

Below are descriptions of the rules to use for the following four situations:

(i) Single program to be built

(ii) Single program to be built from single source file

(iii) Up to three programs to be built

(iv) Arbitrary number of programs to be built

#### 7.1.1. Build single program

The standard rule for generating a program in a directory in which only one program is to be built is ComplexProgramTarget(). It looks like this:

---

[14] To see why, you need to look at the ImakeDependency() and DependDependency() macros in *Imake.rules*. Notice that both macros depend on the value of UseInstalled, so if one is installed, the other must be as well. Also see the definitions of IMAKE and MAKEDEPEND in *Project.tmpl*.

```
/*
 * ComplexProgramTarget - generate rules for compiling and linking the
 * program specified by $(OBJS) and $(SRCS), installing the program and its
 * man page, and generating dependencies.  It should only be used in
 * Imakefiles that describe a single program.
 */
#ifndef ComplexProgramTarget
#defineComplexProgramTarget(program)                                                    @@\
      PROGRAM = program                                                                 @@\
                                                                                        @@\
AllTarget(program)                                                                      @@\
                                                                                        @@\
program: $(OBJS) $(DEPLIBS)                                                            @@\
            RemoveTargetProgram($@)                                                     @@\
            $(CC) -o $@ $(OBJS) $(LDOPTIONS) $(LOCAL_LIBRARIES) $(LDLIBS) $(EXTRA_LOAD_FLAGS)   @@\
                                                                                        @@\
SaberProgramTarget(program,$(SRCS),$(OBJS),$(LOCAL_LIBRARIES), /**/)                    @@\
                                                                                        @@\
InstallProgram(program,$(BINDIR))                                                       @@\
InstallManPage(program,$(MANDIR))                                                       @@\
DependTarget()                                                                          @@\
LintTarget()                                                                            @@\
                                                                                        @@\
clean::                                                                                 @@\
            $(RM) $(PROGRAM)
#endif /* ComplexProgramTarget */
```

ComplexProgramTarget() expands into rules that compile the target program, install it and its manual page, generate header file dependencies, lint the sources, and clean up.

AllTarget() generates an ‘‘all’’ target so that ‘‘make all’’ compiles the program. When ComplexProgramTarget() is the first rule in the *Imakefile*, ‘‘make’’ is equivalent to ‘‘make all’’. The rule following makes the target dependent on its object files and libraries listed in DEPLIBS, and specifies how to link it. SaberProgramTarget() is for Saber C compatibility. InstallProgram() and InstallManPage() install the target and its manual page in their respective directories. DependTarget() and LintTarget() generate rules for dependency list construction and source file linting. The clean target just clobbers the compiled program. This is necessary because, although CleanTarget() is added automatically to the *Makefile* when it’s built and does general clean-up, it doesn’t know the name of the program itself.

Several *make* variables are used in this rule that should be defined before the rule is invoked. SRCS and OBJS should be set to the program’s source files and the correcsponding .o object files. SRCS is used implicitly in the DependTarget() rule, and OBJS is used when linking the target. LOCAL_LIBRARIES is likely to be used for any special libraries needed for the link step.

A typical use of the ComplexProgramTarget() rule might be:

```
SRCS = proga.c progb.c progc.c
OBJS = proga.o progb.o progc.o

ComplexProgramTarget(prog)
```

### 7.1.2. Build single program from single source file

In the case when a single target program is to built in a directory and it comprises only one source file, the source and object file lists can be determined automically. The SimpleProgramTarget() rule can be used. It looks like this:

```
/*
 * SimpleProgramTarget - generate rules for compiling and linking programs
 * that only have one C source file.  It should only be used in Imakefiles
 * that describe a single program.
 */
#ifndef SimpleProgramTarget
#defineSimpleProgramTarget(program)                                    @@\
        OBJS = program.o                                               @@\
        SRCS = program.c                                               @@\
                                                                       @@\

ComplexProgramTarget(program)
#endif /* SimpleProgramTarget */
```

This rule defines SRCS and OBJS for you, then turns into an invocation of ComplexProgramTarget(). You still need to define LOCAL_LIBRARIES if it should be non-empty.

### 7.1.3. Build up to three programs

When up to three targets are to be built, use ComplexProgramTarget_1(), ComplexProgramTarget_2() and ComplexProgramTarget_3(). You must define SRCS1, SRCS2 and SRCS3 to be the source file lists for the three programs, and OBJS1, OBJS2 and OBJS3 to be the object file lists. Then invoke ComplexProgram-Target_1() for the first program, and so on. ComplexProgramTarget_1() looks like this:

```
/*
 * ComplexProgramTarget_1 - generate rules for compiling and linking the
 * program specified by $(OBJS1) and $(SRCS1), installing the program and its
 * man page, and generating dependencies for it and any programs described
 * by $(SRCS2) and $(SRCS3).  It should be used to build the primary
 * program in Imakefiles that describe multiple programs.
 */
#ifndef ComplexProgramTarget_1
#defineComplexProgramTarget_1(program,locallib,syslib)                 @@\
        OBJS = $(OBJS1) $(OBJS2) $(OBJS3)                              @@\
        SRCS = $(SRCS1) $(SRCS2) $(SRCS3)                              @@\
                                                                       @@\
AllTarget($(PROGRAMS))                                                 @@\
                                                                       @@\
program: $(OBJS1) $(DEPLIBS1)                                          @@\
            RemoveTargetProgram($@)                                    @@\
            $(CC) -o $@ $(LDOPTIONS) $(OBJS1) locallib $(LDLIBS) syslib $(EXTRA_LOAD_FLAGS)  @@\
                                                                       @@\
InstallProgram(program,$(BINDIR))                                      @@\
InstallManPage(program,$(MANDIR))                                      @@\
                                                                       @@\
SaberProgramTarget(program,$(SRCS1),$(OBJS1),locallib,syslib)          @@\
                                                                       @@\
DependTarget()                                                         @@\
LintTarget()                                                           @@\
                                                                       @@\
clean::                                                                @@\
            $(RM) $(PROGRAMS)
#endif /* ComplexProgramTarget_1 */
```

Note that SRCS and OBJS are defined for you, and that DependTarget() and LintTarget() are invoked. ComplexProgramTarget_2() and _3() do not do this, so they are simpler. ComplexProgramTarget_2() looks like this:

```
/*
 * ComplexProgramTarget_2 - generate rules for compiling and linking the
 * program specified by $(OBJS2) and $(SRCS2) and installing the program and
 * man page.  It should be used to build the second program in Imakefiles
 * describing more than one program.
 */
#ifndef ComplexProgramTarget_2
#defineComplexProgramTarget_2(program,locallib,syslib)                                  @@\
program: $(OBJS2) $(DEPLIBS2)                                                          @@\
        RemoveTargetProgram($@)                                                        @@\
        $(CC) -o $@ $(LDOPTIONS) $(OBJS2) locallib $(LDLIBS) syslib $(EXTRA_LOAD_FLAGS)  @@\
                                                                                       @@\
SaberProgramTarget(program,$(SRCS2),$(OBJS2),locallib,syslib)                          @@\
                                                                                       @@\
InstallProgram(program,$(BINDIR))                                                      @@\
InstallManPage(program,$(MANDIR))
#endif /* ComplexProgramTarget_2 */
```

### 7.1.4. Build arbitrary number of programs

If you've got an arbitrary number of programs to build, use NormalProgramTarget() for each. It looks like this:

```
/*
 * NormalProgramTarget - generate rules to compile and link the indicated
 * program; since it does not use any default object files, it may be used for
 * multiple programs in the same Imakefile.
 */
#ifndef NormalProgramTarget
#defineNormalProgramTarget(program,objects,deplibs,locallibs,syslibs)                   @@\
program: objects deplibs                                                               @@\
        RemoveTargetProgram($@)                                                        @@\
        $(CC) -o $@ objects $(LDOPTIONS) locallibs $(LDLIBS) syslibs $(EXTRA_LOAD_FLAGS)  @@\
                                                                                       @@\
clean::                                                                                @@\
        $(RM) program
#endif /* NormalProgramTarget */
```

Besides the invocations of NormalProgramTarget(), you must also define SRCS and invoke DependTarget() and LintTarget() yourself. You might also want to invoke AllTarget() before each instance of NormalProgramTarget(), so that ''make all'' will build all the programs.

## 7.2. Miscellaneous Imakefile-writing Observations

- Don't forget ''make depend'' after ''make Makefile''.
- If you intend to use *if* or *for* constructs in an *Imakefile*, you are well advised to copy the way in which they are used in *Imake.rules*. (See the beginning of the *Imake.rules* section of the Appendix for some notes on *if* constructs.)
- If you need to pass special −D's to the C compiler, use the *make* variable DEFINES. If you need to pass special −I's to the C compiler, use the *make* variable INCLUDES. These will be passed to compilations automatically in CFLAGS (see definitions of CFLAGS, ALLDEFINES and ALLINCLUDES in *Imake.tmpl*).
- Some rules may not work as you expect after changing *Imakefile*. In particular, problems can occur the first time you try ''make *target*'' that mysteriously disappear when you try it again. As an instance of this, note that the following command lines are not always equivalent:

```
make Makefile Makefiles
make Makefile ; make Makefiles
```

You normally want to build the ''Makefiles'' target when there are subdirectories. Suppose you add a new subdirectory. This is done by adding it to the definition of SUBDIRS in the current directory's *Imakefile*. You then need to rebuild the *Makefile* so that the definition of SUBDIRS is reset, and then rebuild *Makefile* in each of those subdirectories. The first command line above builds the ''Makefiles'' target using the (old) value of SUBDIRS from the (current) *Makefile*, which is incorrect. The second command line rebuilds the *Makefile*, then runs a second *make* process to build ''Makefiles''. The second process sees the (new) value of SUBDIRS in the (new) *Makefile*, which includes the new subdirectory, and has the intended result.

What can be especially confusing is that if you executed ''make Makefile Makefiles'' twice, it would not do what you expected the first time, but it *would* the second time, since SUBDIRS would be correct on the second try.

## 8. Courting disaster: How to do the wrong thing

*imake* is wonderful for portability when everything is configured properly. However, subtle syntax errors in the configuration files or in imakefiles are often difficult to track down when you begin to make any significant changes to them. This section describes some of the misfortunes that may beset you should you make so bold as to engage in such an endeavor.

For X, the source of problems might be in any of *Imakefile*, *Imake.rules*, *Imake.tmpl*, the platform *.cf* file, or *site.def*. The most likely candidates are *site.def* and the platform file, though, since those are the only ones you're supposed to edit. (If you're writing an *Imakefile*, add that to the list, too.) Just remember that the effects of mistakes in the early files may not be manifest until much later on. Problems may appear to originate at locations far from the actual cause.

(1) *Makefile* trashing

A number of problems during ''make Makefile'' can result in a trashed *Makefile*. If you have *xmkmf* working, you can use that to regenerate the *Makefile* after fixing whatever the problem was. Otherwise you must recover it somehow. If you have a copy of *Makefile* stashed somewhere (you're asking for trouble if you don't), you can use that. If not: the first thing that ''make Makefile'' does is to move the original *Makefile* to *Makefile.bak*; if the new *Makefile* isn't created properly, you can usually recover it with ''cp Makefile.bak Makefile''. Then you can fix the problem and try again. If *Makefile.bak* is trashed as well, you can grab the *Makefile* from another directory at the same level and use that (this works because they both contain the same ''make Makefile'' rule). You can also use a *Makefile* from a directory at another level, but you need to edit the line that sets TOP to reflect where the top project directory is in relation to the current directory.

Moral: make sure *xmkmf* is working first!

(2) Boolean vs. existent/nonexistent *cpp* symbols

Some *cpp* symbols are used in boolean fashion and are defined as YES or NO. These are tested by ''#if *symbol*'' or ''#if !*symbol*''. Others are turned on simply by being defined. These are tested by ''#ifdef *symbol*'' or ''#ifndef *symbol*''. Failure to distinquish the sense in which a symbol is used can lead to problems. It is necessary to define symbols properly *and* to test them properly.

**Example:** YES/NO symbols must be defined properly. SystemV is such a symbol, and the proper test is ''#if SystemV''. If you use ''#define SystemV'' instead of ''#define SystemV YES'', thinking that the former will turn it on, you'll have problems; ''#if SystemV'' turns into just ''#if'' and generates:

cpp: /usr/tmp/tmp-imake.*mmm*:line *nnn*: syntax error

**Example:** YES/NO symbols must be tested properly. If you test such a symbol with #ifdef, the test will always succeed, whether the value is YES or NO; the symbol is defined in *both* cases.

**Example:** Existent/nonexistent symbols must be defined properly. A symbol such as UseInstalled is simply defined (as nothing) or left undefined. If you say ''#define UseInstalled NO'', thinking that will turn it off, you will be surprised. (The test ''#ifdef UseInstalled'' will succeed.)

**Example:** Existent/nonexistent symbols must be tested properly. Such a symbol may be defined as nothing. If you test it, incorrectly, with ''#if symbol'', the test will fail. Use #ifdef.

(3) Rule definition problems

These can occur after ''make Makefile'', if a rule in *Imake.rules* is #define'd with a space between the rule name and the argument list:

```
#define rule(arglist)        right
#define rule (arglist)       wrong!
```

*cpp* will define rulename as ''(arglist)''. When this happens, you'll see

make: line *nnn*: syntax error

and you'll find '' (arglist)'' on line nnn of the *Makefile* because ''rule'' wasn't expanded properly (or at least not the way you expect). Fix it and try again.

Other similar errors can occur if rules are defined or used with spaces anywhere between the end of the macro name to the closing parenthesis of the argument list. This is particularly true if a macro argument is used to generate rule target names or file names. For example, AliasedLibaryTarget() looks like this:

```
#define  AliasedLibraryTarget(libname,alias)                    @@\
AllTarget(lib/**/alias.a)                                       @@\
                                                                @@\
lib/**/alias.a: lib/**/libname.a                                @@\
        $(RM) $@                                                @@\
        $(LN) lib/**/libname.a $@
```

If invoked as ''AliasedLibraryTarget(xyz, libxyz)'' (that is, with a space before ''libxyz'', this will expand to:

```
lib libxyz.a: libxyz.a
    $(RM) $@
    $(LN) libxyz.a $@
```

which won't do very well when *make* gets hold of it. There will appear to be two targets (not one) having a dependency on *libxyz.a*—and one of them is itself! What you want to specify is ''AliasedLibraryTarget(xyz,libxyz)'', which expands to:

```
liblibxyz.a: libxyz.a
    $(RM) $@
    $(LN) libxyz.a $@
```

(4) Failure to supply *cpp* symbol default values

When a *make* variable is equated to a *cpp* symbol, the *cpp* symbol must be defined somewhere, even if it's just defined as nothing. Otherwise the make variable will be set to the literal *cpp* symbol name. That is, if you have ''MAKEVAR=CppSymbol'', it must be preceded somewhere by:

```
#ifndef CppSymbol
#define CppSymbol whatever /* ''whatever'' might be empty */
#endif
```

or MAKEVAR will end up with the literal value ''CppSymbol''.

(5) Macro expansion failure

Suppose you include the following line in your *Imakefile* so that a special version of your library will be compiled:

```
DebuggedAndProfiledLibraryObjectrule()
```

After you regenerate *Makefile*, you try to use it and get the message:

```
Make: Must be a separator on rules line nnn.  Stop.
```

This is symptomatic of a spelling error. If you look closely at the *Imake.rules* file, you'll find that the rule name is actually spelled ''DebuggedAndProfiledLibraryObjectRule()''. If you don't spell a rule name correctly, it won't be expanded. Fortunately, these problems are easy to find, because at least the error message tells you where to look. Unfortunately, you can't say ''make Makefile'' after you fix *Imakefile*, because the effect of the error is to make your *Makefile* unusable. Use *xmkmf*.

(6) Forgetting ''@@\''

If you modify a rule by inserting a line into the middle, and forget the ''@@\'' marker at the end, it won't expand properly. Or if you add a line to the end, and forget to add the marker to what was formerly the last line, you'll get the same result.

(7) The never-ending *make*.

Place the following in your *Imakefile*, then rebuild the *Makefile*:

```
# define IHaveSubdirs
SUBDIRS =
```

If you don't get a syntax error from *make* when you try any of the recursive targets (clean, Makefiles, depend, etc.), *make* will probably loop forever because of the empty definition of SUBDIRS.

(8) Using a rule name in a *make*-style comment

If there is a rule named XYZTarget(), and you include a comment like the following in an *Imakefile*, the reference to XYZTarget() will still be expanded, since it's not enclosed in a C-style comment.

```
/**/# The effect of the XYZTarget() rule is to...
```

The resulting *Makefile* will often be useless.